

PATENT COOPERATION TREATY

PCT

NOTIFICATION OF ELECTION

(PCT Rule 61.2)

From the INTERNATIONAL BUREAU

To:

Commissioner
 US Department of Commerce
 United States Patent and Trademark
 Office, PCT
 2011 South Clark Place Room
 CP2/5C24
 Arlington, VA 22202
 ETATS-UNIS D'AMERIQUE
 in its capacity as elected Office

Date of mailing (day/month/year) 05 June 2001 (05.06.01)	
International application No. PCT/AU00/01101	Applicant's or agent's file reference P16608
International filing date (day/month/year) 15 September 2000 (15.09.00)	Priority date (day/month/year) 16 September 1999 (16.09.99)
Applicant DUANCE, Roger, Kent	

1. The designated Office is hereby notified of its election made:

☒ in the demand filed with the International Preliminary Examining Authority on:
 05 April 2001 (05.04.01)

☐ in a notice effecting later election filed with the International Bureau on:

2. The election ☒ was
☐ was not

made before the expiration of 19 months from the priority date or, where Rule 32 applies, within the time limit under Rule 32.2(b).

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland Facsimile No.: (41-22) 740.14.35	Authorized officer Charlotte ENGER Telephone No.: (41-22) 338.83.38
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PATENT COOPERATION TREATY

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NOTIFICATION OF THE RECORDING
OF A CHANGE(PCT Rule 92bis.1 and
Administrative Instructions, Section 422)

From the INTERNATIONAL BUREAU

To:

MADDERNS
1st Floor
64 Hindmarsh Square
Adelaide, S.A. 5000
AUSTRALIE

Date of mailing (day/month/year) 07 June 2001 (07.06.01)	IMPORTANT NOTIFICATION
Applicant's or agent's file reference P16608	
International application No. PCT/AU00/01101	International filing date (day/month/year) 15 September 2000 (15.09.00)

1. The following indications appeared on record concerning:		
<input checked="" type="checkbox"/> the applicant	<input type="checkbox"/> the inventor	<input type="checkbox"/> the agent <input type="checkbox"/> the common representative
Name and Address BRITAX RAINSFORDS PTY LTD Sherriffs Road Lonsdale, S.A. 5160 Australia	State of Nationality AU	State of Residence AU
	Telephone No.	
	Facsimile No.	
	Teleprinter No.	
2. The International Bureau hereby notifies the applicant that the following change has been recorded concerning:		
<input type="checkbox"/> the person	<input checked="" type="checkbox"/> the name	<input type="checkbox"/> the address <input type="checkbox"/> the nationality <input type="checkbox"/> the residence
Name and Address SCHEFENACKER VISION SYSTEMS AUSTRALIA PTY LTD Sherriffs Road Lonsdale, S.A. 5160 Australia	State of Nationality AU	State of Residence AU
	Telephone No.	
	Facsimile No.	
	Teleprinter No.	
3. Further observations, if necessary:		
4. A copy of this notification has been sent to:		
<input checked="" type="checkbox"/> the receiving Office	<input type="checkbox"/> the designated Offices concerned	
<input type="checkbox"/> the International Searching Authority	<input checked="" type="checkbox"/> the elected Offices concerned	
<input checked="" type="checkbox"/> the International Preliminary Examining Authority	<input type="checkbox"/> other:	

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland	Authorized officer V. Gross (Fax 338.87.40)
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INTERNATIONAL COOPERATION TREATY
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INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

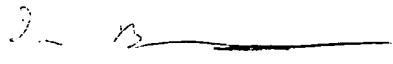
REC'D 26 APR 2001

WIPO

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Applicant's or agent's file reference 16608 WGM:NH	FOR FURTHER ACTION	See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416).
International Application No. PCT/AU00/01101	International Filing Date (<i>day/month/year</i>) 15 September 2000	Priority Date (<i>day/month/year</i>) 16 September 1999
International Patent Classification (IPC) or national classification and IPC Int. Cl. ⁷ G01D 5/14, B60R 1/072		
Applicant BRITAX RAINSFORDS PTY LTD et al		

1.	This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36.	
2.	This REPORT consists of a total of 3 sheets, including this cover sheet. <input type="checkbox"/> This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT). These annexes consist of a total of sheet(s).	
3.	This report contains indications relating to the following items: I <input checked="" type="checkbox"/> Basis of the report II <input type="checkbox"/> Priority III <input type="checkbox"/> Non-establishment of opinion with regard to novelty, inventive step and industrial applicability IV <input type="checkbox"/> Lack of unity of invention V <input checked="" type="checkbox"/> Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement VI <input type="checkbox"/> Certain documents cited VII <input type="checkbox"/> Certain defects in the international application VIII <input type="checkbox"/> Certain observations on the international application	

Date of submission of the demand 5 April 2001	Date of completion of the report 12 April 2001
Name and mailing address of the IPEA/AU AUSTRALIAN PATENT OFFICE PO BOX 200, WODEN ACT 2606, AUSTRALIA E-mail address: pct@ipaustalia.gov.au Facsimile No. (02) 6285 3929	Authorized Officer  I.A.BARRETT Telephone No. (02) 6283 2189

I. Basis of the report

1. With regard to the **elements** of the international application:*
- ☒ the international application as originally filed.
- ☐ the description, pages , as originally filed,
 pages , filed with the demand,
 pages , received on with the letter of
- ☐ the claims, pages , as originally filed,
 pages , as amended (together with any statement) under Article 19,
 pages , filed with the demand,
 pages , received on with the letter of
- ☐ the drawings, pages , as originally filed,
 pages , filed with the demand,
 pages , received on with the letter of
- ☐ the sequence listing part of the description:
 pages , as originally filed
 pages , filed with the demand
 pages , received on with the letter of
2. With regard to the **language**, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item.
These elements were available or furnished to this Authority in the following language which is:
- ☐ the language of a translation furnished for the purposes of international search (under Rule 23.1(b)).
- ☐ the language of publication of the international application (under Rule 48.3(b)).
- ☐ the language of the translation furnished for the purposes of international preliminary examination (under Rules 55.2 and/or 55.3).
3. With regard to any **nucleotide and/or amino acid sequence** disclosed in the international application, was on the basis of the sequence listing:
- ☐ contained in the international application in written form.
- ☐ filed together with the international application in computer readable form.
- ☐ furnished subsequently to this Authority in written form.
- ☐ furnished subsequently to this Authority in computer readable form.
- ☐ The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.
- ☐ The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished
4. ☐ The amendments have resulted in the cancellation of:
- ☐ the description, pages
- ☐ the claims, Nos.
- ☐ the drawings, sheets/fig.
5. ☐ This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**

* Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rules 70.16 and 70.17).

** Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement**1. Statement**

Novelty (N)	Claims 1-8	YES
	Claims	NO
Inventive step (IS)	Claims 1-8	YES
	Claims	NO
Industrial applicability (IA)	Claims 1-8	YES
	Claims	NO

2. Citations and explanations (Rule 70.7)

The prior art does not show a position sensing apparatus where a sensor output voltage is converted to a current by a voltage to current converter, and the sensor supply voltage and the voltage to current reference voltage have a ratiometric relationship. This is considered to be novel and involves an inventive step.

(19) World Intellectual Property Organization
International Bureau



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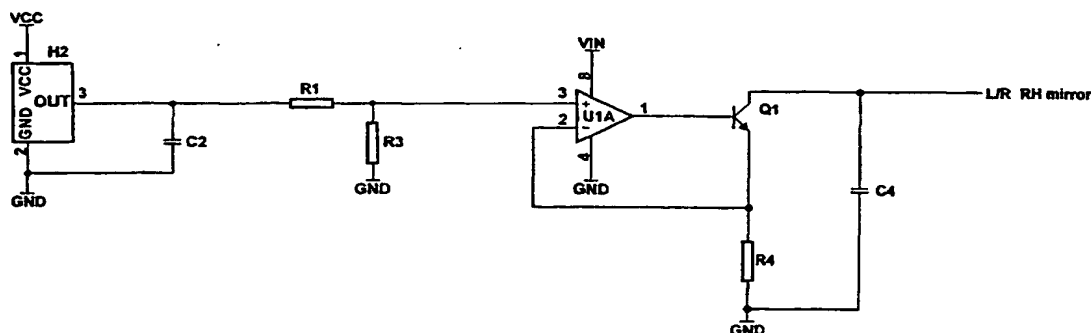
- (51) International Patent Classification⁷: **G01D 5/14**, (74) Agent: **MADDERN**s; 1st Floor, 64 Hindmarsh Square, Adelaide, S.A. 5000 (AU).
B60R 1/072
- (21) International Application Number: **PCT/AU00/01101** (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.
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Published:

— With international search report.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: **SENSOR MEMORY ELECTRONIC CIRCUIT**



(57) Abstract: Position sensing and control of the displacement of a first member relative to a second member particularly in a vehicular environment is aided by having a ratiometric relationship between the voltage supply for the sensor and the measuring/control portion of the apparatus. In an embodiment, the relative position of a rear view mirror to its housing is controlled. To facilitate the return of a mirror to a preferred position the conversion of sensor voltage output to a current and the storage the current of one or more preferred positions allows for the repeatable setting of the rear view mirror. Over and under voltage protection of the sensor supply voltage provides for consistent measurement and control characteristics.

SENSOR MEMORY ELECTRONIC CIRCUIT

This invention relates to position sensing and control and in particular to a method and circuit to measure a displacement of a first member relative to a second member and when used with an associated stored measurement, to control the return of a displaced first member to a former or predetermined position relative to a second member.

BACKGROUND

In this specification the method and circuit described will be related to rear view mirrors for motor vehicles of the type in which the orientation of the mirror glass is adjustable relative to the mirror housing by means of two screw jack drives arranged to adjust the orientation of the mirror glass about mutually orthogonal axes.

However, it would be possible for the circuit elements and method disclosed herein to be used with any moveable mechanism which can be sensed and then controlled to move to predetermined positions.

One example of a sensor system representing linear displacement is disclosed in European Patent 0907068 entitled "Hall Effect Sensor" in the name of Britax Rainsfords Pty Ltd, the applicant in this matter, that is hereby included in this specification by reference.

Furthermore European Patent 0549173 entitled "Rear View Mirror Assembly for Motor Vehicle" also in the name of Britax Rainsfords Pty Ltd and is referred to in European Patent 0907068, that is also hereby included in this specification by reference.

European Patent 0549173 discloses a rear view mirror assembly for a motor vehicle of the type in which a reflective member is mounted for angular movement on a pivotal

mounting attached to a mirror housing, with the drive housing located in the mirror housing having a projecting jacking screw which is pivotally coupled to the rear surface of the reflective mirror at a location remote from the mirror's pivotal mounting.

A problem with any position sensing and control arrangement, and in particular for vehicular environments, is the variability of the supply of power to the drive circuit as well as the position sensors of the arrangement. Power supply variability and the nature of the contact between electrical circuits within a vehicle environment can result in sensing control inaccuracies and potential damage to the control circuitry. Furthermore when voltage sensing from a potentiometer (variable voltage divider) is used to determine relative position sensing such as linear or rotational displacement, accuracy can be detrimentally affected as a result of voltage variation, mechanical wear and environmental influences.

Voltage variation can occur when the vehicle experiences hot and cold environments, and in addition there is the further complicating factor of voltage noise spikes caused by radio frequency interference (RFI). All these factors can greatly affect the accuracy and hence repeatability of positional displacement because the voltage measurements used are not reliable.

Furthermore, voltage based systems are designed to have "wetting currents" to overcome the expected high resistance joints in cabling and in doing so also minimise transient voltage drops within the circuits caused by contact resistance in the cabling joints. The existence of "wetting currents" further compromise the use of voltage measurements to determine positional displacement.

It is an aim of this invention to minimise or eliminate these types of problems and design compromises.

BRIEF DESCRIPTION OF THE INVENTION

In a broad aspect of the invention a position sensing and control apparatus comprises a first supply voltage means supplying a first supply voltage; a sensor powered by said supply voltage and operatively connected to a first member moveable relative to a second member to produce a voltage representative of the position of said first member relative to said second member; a voltage to current converter having a reference voltage supply for converting the voltage output of said sensor to a current wherein said first supply voltage has a ratiometric relationship with said reference voltage.

In a further aspect of the invention the position sensing and control apparatus further comprises storage means for storing values of said current representative of a state of a respective sensor; a control circuit for controlling the movement of said first member, arranged to move said first member until said voltage to current converter output is substantially the same as a said stored value of current.

In yet a further aspect of the invention over voltage produced by said first supply voltage is prevented from being supplied to said sensor when a greater than predetermined voltage is detected or when said first supply voltage is smaller than a predetermined voltage.

These and other advantages of the invention will be apparent from an understanding of the information described herein.

A specific embodiment of the invention will now be described in some further detail with reference to and as illustrated in the accompanying figures. This embodiment is illustrative, and is not meant to be restrictive of the scope of the invention.

Suggestions and descriptions of other embodiments may be included but they may not be illustrated in the accompanying Figures alternatively features of the invention may be shown in the Figures but not described in the specification.

BRIEF DESCRIPTION OF THE FIGURES

Figure 1 is a circuit diagram of a voltage to a current converter according to the invention; and

Figure 2 is a circuit diagram of a voltage to current converter according to the invention; and

Figure 3 is a supply voltage controller circuit according to the invention.

DETAILED DESCRIPTION OF AN EMBODIMENT OF THE INVENTION

As described previously the circuits described herein are applied to rear view mirror assemblies but would be useful in overcoming the same or similar problems in other environments or with other types of sensors.

In this embodiment of a rear view arrangement there are two major components.

First of these components is a rear view mirror assembly incorporating a two axis sensing system coupled to the mirror of the assembly. Vehicles typically have two such assemblies, one located on the driver side and one on the passenger side of the vehicle, both for use by the driver to view rearwardly of the vehicle.

Each rear view mirror assembly can be considered to be a sensor/transmitter component in the context of the rear view mirror assembly.

A typical rear view mirror assembly incorporates two drive systems (commonly, but not limited to, two Direct Current (D. C.) motors and associated mechanical jack systems as described previously) capable of moving a mirror in two mutually orthogonal axes relative to the mirror housing.

Some mirrors require adjustment in only one axis thus only one D. C. motor and associated mechanical jack system would be used.

A rear view mirror assembly also comprises a sensor arrangement for sensing the relative position of the mirror to the mirror housing and typically one sensor is used for each axis of motion of the mirror.

A further element of the mirror assembly is a circuit which transforms the sensor output to a signal for transmission external of the mirror housing.

A second component of a rear view arrangement is a rear view mirror controller typically located in the vehicle and commonly incorporated into the seat positioning module. This component can be considered the receiver/processor for the purposes of this description.

The receiver/processor receives a signal representative of a relative measure of the mirror position in both axes and also has the ability to drive the mirror in a direction relative to both axes (left/right and up/down) via the drive systems installed in the rear view mirror assembly to new positions or to stored predetermined positions.

The mirror assembly is preferably supplied with a regulated voltage supply (typically +5V which is regulated at the receiver/processor end) so that the associated sensor/transmitter circuitry can provide a two axes measure of the mirror surface position relative to the mirror housing. Each axis position is communicated to the receiver/processor in the form of a signal comprising, in this embodiment, a current value which is representative of the relative position of the mirror to the mirror housing. The measurement is sent in this preferred form as it is insensitive to the resistance of the contacts in the connectors used between the mirror assembly and the receiver/processor. A common problem that arises when using voltage measurement values are transmitted as representative of relative position, is that if the transmitter conducts any significant current, which is desirable to "wet" contacts,

then the position as measured at the receiver/processor will likely vary dependent on the actual resistance of the connectors. Connectors perform very poorly at extremely low currents that are desirable for an accurate voltage measurement system.

The receiver/processor accepts commands from the vehicle driver to either "save" the current position, or to "recall" a previously saved position.

A "save" command stores in a memory, as current values, the current mirror position measurement by recording the relative position of both axes, as supplied from the sensor/transmitter component of the rear view mirror assembly. The receiver/processor will preferably have a plurality of memory locations into which can be stored a plurality of unique positions of the mirror head for one or more rear view mirrors, as commanded by the driver.

On a "recall" command to a particular stored position from the driver, the receiver/processor proceeds to drive the mirror via the drive system in such a direction so as to equate the present current values as measured by the mirror assembly with the stored position current values. It does this for both axes either simultaneously or sequentially for each mirror commanded to be changed. The result is that the mirror returns to a preferred stored position and does so by making the measured position correspond to the stored position for both axes of movement. It should be noted that a single axis mirror would only require one current measurement for each position stored.

The sensor/transmitter of the rear view mirror assembly also incorporates a circuit to protect the sensing circuit and drive circuits against over voltages which can occur on automotive voltage supply rails. As it is desirable to supply the mirror assembly, which preferably uses a Hall Effect sensor measurement circuit, with the same voltage as is supplied at the receiver/processor, a "switch" is interposed between the receiver/processor and the supply voltage to the mirror assembly. This switch,

preferably electronic, is rapidly switched off to cease the supply of power to the rear view mirror assembly in the event of an over voltage as measured on the supply to the mirror assembly. It is desirable that the supply voltage to the sensor/transmitter component tracks the voltage of the receiver/processor as that voltage is then useable as a reference for the voltage to be supplied to the sensor/transmitter.

The output voltages from the sensors in the external rear view mirror housing are thus actually a percentage of the supplied voltage to those sensors, and not an absolute voltage. As the receiver/microprocessor also measures the incoming voltage as a percentage of the applied voltage, these two circuits are thus "ratiometric" and are insensitive to the actual rail voltage supplied by the vehicle as long as that rail voltage is within acceptable values. Although it is preferable to use a Hall Effect sensor it is possible to use the industry standard potentiometer sensor as well as any other suitable sensor device.

It is believed by the inventors that the use of current to measure and compare mirror position actuation has not heretofore been used in a vehicle, where is it typical for mirror systems simply to use voltage sensing from a potentiometer (variable voltage divider) to determine mirror position on one or more axes.

The prior methods have the disadvantage of having to incorporate elements subject to wear over time which can affect the sensitivity of these circuits to environmental influences, which consequently affect the accuracy and repeatability of the circuits. Prior approaches to the design of rear view mirror assemblies have involved the desire to use extremely low current draw from the potentiometer so as to avoid influencing the voltage measurement and thus the sensed position. However, this is at odds with the requirement to provide sufficient current through the connecting cables to reduce the effects of contact resistance in the circuit which is typically overcome by providing a "wetting current".

In this embodiment Hall Effect sensors are preferably used to measure the relative displacement of a first member (the mirror) about an axis of the pivotable mirror with reference to a second member (the mirror housing) by sensing the magnitude of a magnetic field from a magnet attached to the moving mirror relative to a sensor stationary on the mirror housing. The Hall Effect sensor output voltage is converted to a current proportional to the magnetic field, and hence, mirror position relative to the mirror housing is available for transmission to the receiver/processor. As discussed, the sensor voltage is produced as a proportion of a voltage which is in constant proportion to the voltage existing at the receiver/processor. The current produced with the voltage to current converter, is also operated by a voltage referenced to the voltage at the receiver/processor, thus the current provided by the voltage to current converter is an repeatable representation of the mirror position which is immune to vehicle voltage supply variations which are not uncommon.

Over voltage conditions are controlled by the use of a fast acting switch to switch "off" the supply of voltage to the Hall sensors whenever an over voltage is detected. The supply is automatically switched back on when the over voltage returns to a safe level. All the relevant circuit thus remains essentially ratiometric within preset limits thus eliminating or reducing the effect of different supply voltages at each end of the measurement and control circuits. If two voltage regulators with different characteristics were used at the sensor/transmitter and the receiver/processor, problems as discussed would arise. In this embodiment the voltage supply at the receiver/processor acts as a comparison voltage ("reference voltage") and the microprocessor which is used for converting analogue current values to digital signals, the same reference voltage is also used.

Referring to Figures 1, 2 and 3, the mirror assembly circuit comprises two identical position conditioner circuits (Figures 1 and 2) and a supply voltage conditioner circuit (Figure 3).

The position conditioner circuit determines the position of the mirror glass by using the voltage output of a Hall Effect sensor. The Hall Effect sensor attached to the mirror housing produces an output voltage representative of the position of a magnet connected to the moveable mirror glass while being supplied a substantially constant voltage. Refer to European patents 0907068 and 2549173 as previously disclosed for further technical details regarding the operation of a preferred Hall Effect sensor.

The sensor output voltage is converted to an output current via U1A and Q1 and this current is transmitted to the receiver/transmitter which is, as previously mentioned, part of the seat module of a vehicle. Both the left/right movement of the mirror glass relative to the mirror housing and the up/down movement of the glass relative to the mirror housing are sensed using sensor and magnet pairs arranged to detect relative movement, one pair for each axis of movement.

The Hall Effect sensor voltage is divided by 10 by resistors R1 and R3 (which are high precision value resistors) and presented to the positive input of an operational amplifier U1A. The output of this amplifier is high (voltage) when the positive input is greater than the negative input. The output voltage is supplied to the base of a transistor Q1 connected as an emitter follower. As the output voltage of U1A increases, so does the voltage appearing across the resistor R4, which is fed back to the negative input of U1A. When the fed back voltage reaches the voltage existing at the positive input (the Hall Effect sensor voltage divided by 10) the output voltage of U1A is decreased until it maintains a condition whereby the two inputs to the operational amplifier are equal. As the Hall Effect sensor voltage is divided by a factor of 10, there will typically be 0.25 volts appearing across R4 for an input voltage of 2.5 volts. This results in a current of 2.5 milliamps through R4 the range of which can be varied by altering R4.

The collector current (output) of Q1 is effectively the same as the emitter current, that which flows through the resistor R4 (less the base current, which in this circuit is typically 1/500 of the emitter current).

As a result, there is an output current which is an accurate representation of the Hall Effect sensor voltage and hence the mirror position along one of its axes relative to the mirror housing. In this embodiment there exists a scaling of 1 milliamp per volt output from the Hall Effect sensor, which can be altered, if required, by varying the divider resistors and the current sense resistor.

The second axis of the mirror position is conditioned by a second circuit consisting of U1B and Q2 and their associated components as is depicted in Figure 2 in the same manner as that described for the circuit in Figure 1.

At the receiving end of the output line of the circuits of Figure 1 and Figure 2 (at the receiver/processor, not shown) the line is terminated with for example a 1000 ohm resistor (not shown) connected to a +5 volt supply which is designated as V_{in} on the circuit depicted in Fig 3. The current flowing through this resistor, and hence the voltage appearing across it, is controlled by the above described sensor/transmitter circuit and is representative of the position of the mirror glass relative to the mirror housing.

Thus, with such a circuit arrangement, even if there is significant resistance in the connectors between the mirror (sensor/transmitter) and the receiver/processor in the seat module (a common problem), there is no change to the value of the current flowing. Therefore the sensed voltage at the receiver end is communicated to the receiver and remains an accurate representation of the mirror position relative to the mirror housing.

As discussed the third part of the mirror assembly circuit comprises a voltage controlled switch supplying the circuit (Fig 3).

The normal method of suppressing unwanted over voltage conditions commonly occurring in vehicles is to use a voltage regulator integrated circuit chip, which is commonly available, or alternatively using such a chip in combination with a zener diode, which acts as a crude voltage regulator. In both cases the voltage presented as a voltage supply is significantly reduced from the incoming voltage, is imprecise in its over voltage control due to component characteristic variabilities and environmental conditions or demands expensive or large electronic components to achieve the desired objectives of a stable regulated power supply for every component of a rear view mirror assembly .

Hall Effect sensors are particularly sensitive to over voltage and thus in this embodiment it is preferable to have the voltage supplied to the Hall Effect sensors closely tracking the incoming voltage which is typically regulated at the receiver/processor end. If this condition is satisfied the rear view mirror assembly circuit will not be sensitive to small variations in the supply voltage, as the "same" (regulated) voltage can be supplied to the receiver/processor and in particular to circuits such as a microprocessor analog-to-digital converter as a reference voltage as that voltage which is supplied to the Hall Effect sensors. What is thus provided in this embodiment is known as a "ratiometric" circuit, in that the voltage output from the Hall Effect sensor is a ratio of the supply voltage, and the analog-to-digital converter microprocessor input at the receiver/processor end is expressed as a ratio of that same voltage. This arrangement dramatically decreases the sensitivity of the system to small variants in the typically +5 volts regulated supply.

In the embodiment of the over voltage protection circuit depicted in Fig 3, U2 is a specialised component typically used to function as a microprocessor reset for low voltages. It is used in this embodiment in a novel way, to switch "on" transistor Q3 in the saturated condition so as to divert or switch incoming voltage through to the remainder of the circuit which includes the Hall Effect sensors. The saturated

condition of a transistor ensures the lowest possible voltage drop across the transistor during its "on" state.

When U2 detects a voltage above a predetermined voltage say 5.5 volts, it switches "off" the transistor Q3 and hence the supply voltage to the rest of the circuit.

The result is that the voltage supplied to the circuit is kept a close representation of the incoming voltage except when it is raised, for whatever reason, above a predetermined voltage, say 5.5 volts (a fault condition), when it is totally switched "off" until the incoming voltage decreases to below, a predetermined voltage, say approximately 5.2 volts. The decision to use these upper and lower limits is based on the typical operating conditions of the chip U2 and experimental data based on that chip.

Zener Diode ZD1 and R11 serve to protect U2 from excessive voltages. Various capacitors are used in the circuit to protect against transient voltage peaks and to suppress possible interfering oscillations such as those caused by RFI.

It will be appreciated by those skilled in the art, that the invention is not restricted in its use to the particular application described and neither is the present invention restricted in its preferred embodiment with regard to the particular elements and/or features described or depicted herein. It will be appreciated that various modifications can be made without departing from the principles of the invention, therefore, the invention should be understood to include all such modifications within its scope.

THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

1. A position sensing and control apparatus comprising:
 - a first supply voltage means supplying a first supply voltage;
 - a sensor powered by said supply voltage and operatively connected to a first member moveable relative to a second member to produce a voltage representative of the position of said first member relative to said second member; and
 - a voltage to current converter having a reference voltage supply for converting the voltage output of said sensor to a current wherein said first supply voltage has a ratiometric relationship with said reference voltage.
2. A position sensing and control apparatus according to claim 1 further comprising:
 - a storage means for storing values of said current representative of a state of a respective sensor; and
 - a control circuit for controlling the movement of said first member, arranged to move said first member until said voltage to current converter output is substantially the same as a said stored value of current.
3. A position sensing and control apparatus according to claim 1 further comprising:
 - a voltage detector for detecting said first supply voltage that prevents said first supply voltage from being supplied to said sensor when a greater than predetermined voltage is detected.
4. A position sensing and control apparatus according to claim 3 wherein said voltage detector further comprises a switch interposed between said control circuit and said first supply voltage means used to isolate said control circuit and said supply voltage means when said first supply voltage is smaller or greater than predetermined voltages.

5. A position sensing and control apparatus according to claim 4 wherein said switch comprises a transistor the base of which is connected to and controlled by an output voltage of said voltage detector means.
6. A position sensing and control apparatus according to claim 1 wherein said sensor is a Hall Effect device.
7. A position sensing and control apparatus according to claim 1 wherein said voltage to current means comprises an operational amplifier having a positive and negative input for receiving a voltage output from said sensor at said positive input and an output of said operational amplifier being high when said positive input is greater than said negative input, the output of said operational amplifier being input to the base of an emitter follower transistor the emitter of which is connected to a resistive means to ground and above which a voltage feed back circuit to said negative input of said operational amplifier is provided, wherein as said feedback voltage reaches the value of said input voltage to said positive input the output voltage of said operational amplifier decreases until said positive and negative voltage inputs are the same the result of which there is, as a result of the ratiometric relationship between the reference between said first supply voltage of said sensor and said reference voltage supply of said voltage to current means, the current through said resistance means is representative of said sensor position.
8. A position sensing and control apparatus according to claim 7 wherein the collector current of said emitter follower transistor is substantially the same as said current through said resistance means.

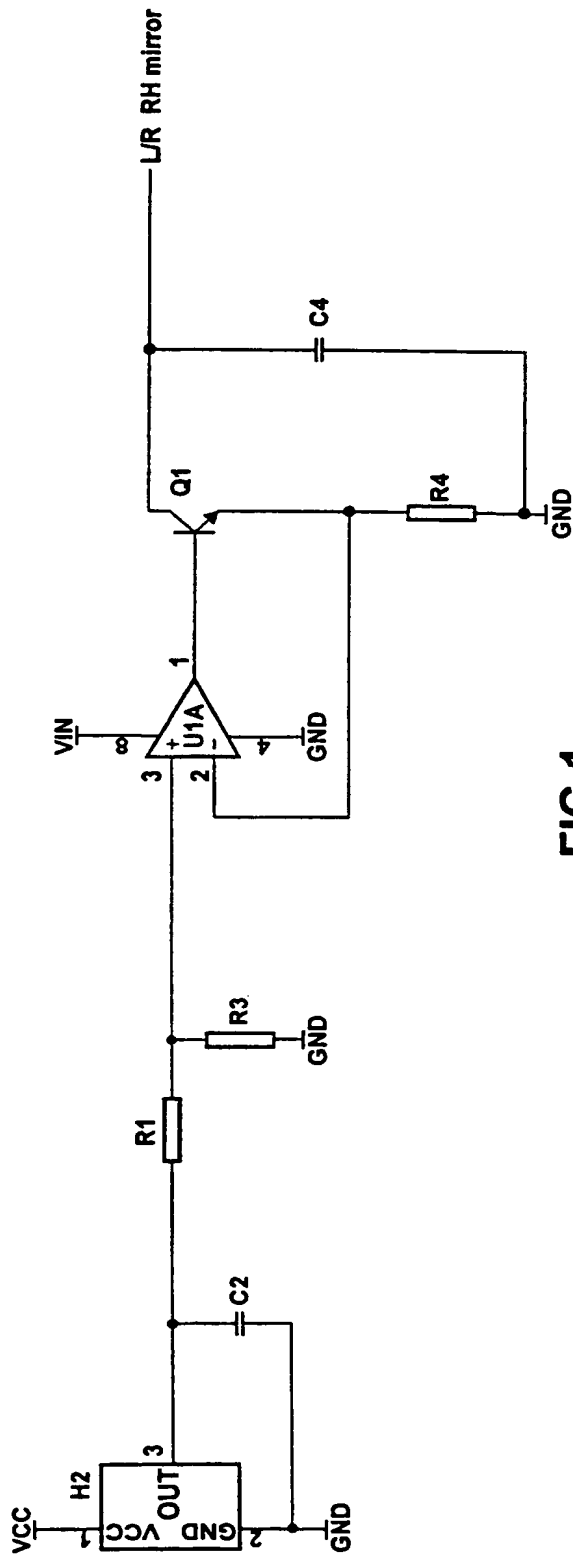


FIG 1

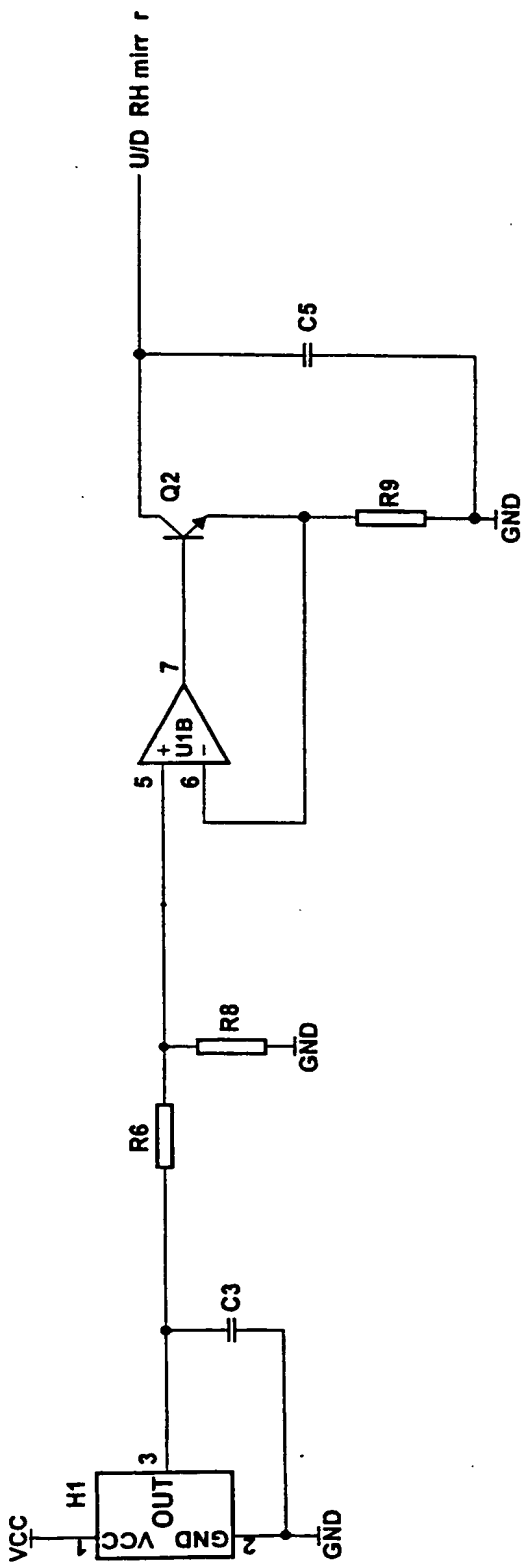


FIG 2

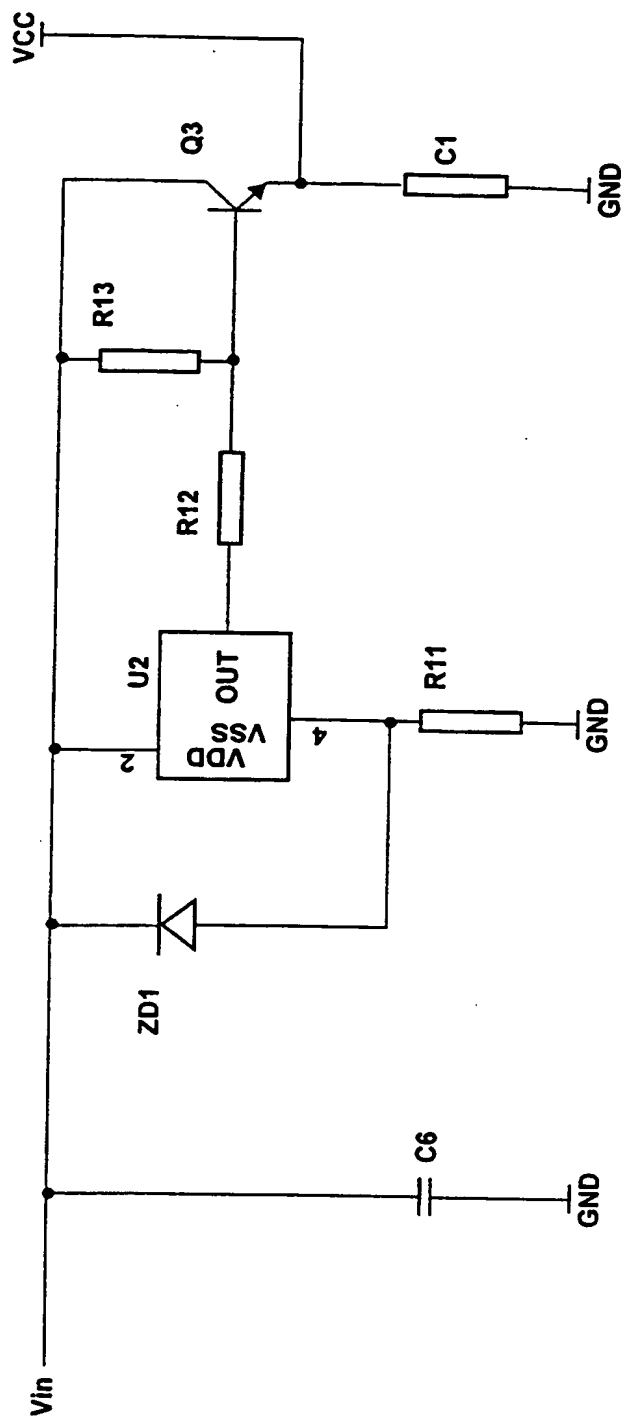


FIG 3

INTERNATIONAL SEARCH REPORT

International application No.
PCT/AU00/01101

A. CLASSIFICATION OF SUBJECT MATTER

Int. Cl. ⁷: G01D 5/14, B60R 1/072

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
B60R 1/-, G01D 5/-, G01B 7/-

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
DWPI, JAPIO; IPC B60R 1/-, G01D 5/-, G01B 7/-, G05
with keywords: voltage, current, ratiometric, position, sensor

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 290231 A2 (Britax Rainsfords Pty Ltd) 9 November 1988 See the abstract	
A	EP 115593 A2 (Dresser Industries, Inc) 15 August 1984 See the abstract	
A	GB 2102364 A (Hohe KG) 2 February 1983 See the abstract	

☐ Further documents are listed in the continuation of Box C ☒ See patent family annex

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance
"E" earlier application or patent but published on or after the international filing date
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"O" document referring to an oral disclosure, use, exhibition or other means
"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"&" document member of the same patent family

Date of the actual completion of the international search
3 November 2000

Date of mailing of the international search report
10 NOV 2000

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/AU00/01101

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report				Patent Family Member			
EP	290231	AU	15538/88	JP	1047636	NZ	224444
		US	4856885				
EP	115593	BR	8400019	CA	1203867	JP	59136615
		US	4538063				
GB	2102364	FR	2509675	IT	1192477	JP	58026653
		SE	8107036	DE	3226435		
END OF ANNEX							